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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/229,592
	Filing Date	January 13, 1999
	First Named Inventor	Brian S. Doyle
	Group Art Unit	2815
	Examiner Name	Brock II, P.
Total Number of Pages in This Submission		Attorney Docket Number 42390.P5578

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	John Travis Reg. No. 43,203
Signature	<i>John Travis</i>
Date	6-12-02

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE THE

APPLICATION OF: Brian S. Doyle

ART UNIT: 2815

APPLICATION No.: 09/229,592

EXAMINER: Brock II, P.

FILED: January 13, 1999

TITLE: A TRANSISTOR WITH REDUCED
SERIES RESISTANCE JUNCTION
REGIONS

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SUBMISSION OF PROPOSED DRAWING AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Applicants respectfully submit for the Examiner's approval the enclosed two (2) sheets of drawings as red-lined to incorporate the proposed amendment. In view of the informal nature of the drawings, applicants maintain that the drawings may serve as both a marked up version and a clean version of the drawings after amendment.

No fee is believed due with this submission, but if necessary the Commissioner is hereby authorized to credit any overpayment or to charge any fees or fee deficiencies under 37 C.F.R. § 1.16 and § 1.17 in connection with this communication to our Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY SOKOLOFF TAYLOR & ZAFMAN LLP

Date: 6-12, 2002

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